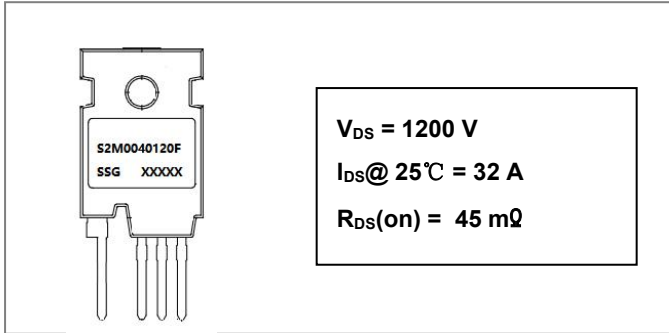


# S2M0040120F

## 1200V SiC POWER MOSFET



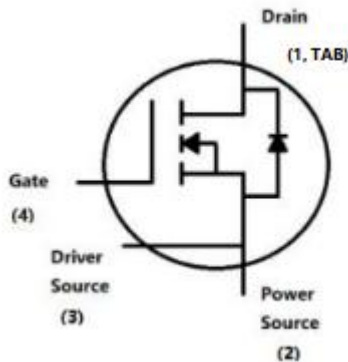
### Description

S2M0040120F is a single SiC Power MOSFET packaged in TO-247-4 full case. The device is a high voltage n-channel Enhancement mode MOSFET that has very low total conduction losses and very stable switching characteristics over temperature extremes. The S2M0040120F is ideal for energy sensitive, high frequency applications in challenging environments.

### Features

- Positive temperature characteristics, easy to parallel.
- Low on-resistance Typ.  $R_{DS(on)} = 45m\Omega$  .
- Fast switching speed and low switching losses.
- Very fast and robust intrinsic body diode.
- Process of non-bright Tin electroplatin
- “-A” is an AEC-Q101 qualified device

### Circuit Diagram



### Applications

- EV Fast Charging Modules
- EV On Board Chargers
- Solar Inverters
- Online UPS/Industrial UPS

### Maximum Ratings(T=25°C unless otherwise specified)

Characteristics	Symbol	Condition	Max.	Units
Drain Source Voltage	$V_{DSS}$	$V_{GS} = 0V, I_{DS} = 100\mu A, T_j = 25^\circ C$	1200	V
Gate Source Voltage	$V_{GSS}$	$T_j = 25^\circ C$ , Absolute maximum values, AC ( $f > 1Hz$ )	-10 to 25	V
Gate Source Voltage	$V_{GSOP}$	$T_j = 25^\circ C$ Recommended Operational Values	-5 to 20	V
Continuous Drain Current	$I_D$	$V_{GS} = 20V, T_j = 25^\circ C$	32	A
	$I_D$	$V_{GS} = 20V, T_j = 100^\circ C$	22	A
Pulsed Drain Current	$I_{D,pulse}$	Pulse width $t_P$ limited by $T_{jmax}$	160	A
Power Dissipation	PD	$TC=25^\circ C, T_j = 175^\circ C$	120	W

**Electrical Characteristics(T=25°C unless otherwise specified)**

Characteristics	Symbol	Condition	Min.	Typ.	Max.	Units
Drain Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 100\mu A$	1200			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 10mA$	2.0	2.8	4.0	V
		$V_{DS} = V_{GS}, I_D = 10mA, T_J = 175^\circ C$		1.8		V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 1200V, V_{GS} = 0V$		1	100	$\mu A$
Gate Source Leakage Current	$I_{GSS}$	$V_{GS} = 20V, V_{DS} = 0V$			250	nA
Drain Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 20V, I_D = 40A$		45	52	m $\Omega$
		$V_{GS} = 20V, I_D = 40A, T_J = 175^\circ C$		73		m $\Omega$
Transconductance	gfs	$V_{DS} = 20V, I_{DS} = 40A$		10		S
		$V_{DS} = 20V, I_{DS} = 40A, T_J = 175^\circ C$		12		S
Input Capacitance	$C_{ISS}$	$V_{GS} = 0V,$		1904		pF
Output Capacitance	$C_{OSS}$	$V_{DS} = 1000V$		108		
Reverse Transfer Capacitance	$C_{RSS}$	$V_{AC} = 25mV$ $f = 1MHz$		6		
$C_{OSS}$ Stored Energy	$E_{OSS}$			72.9		$\mu J$
Turn-On Switching Energy	$E_{ON}$	$V_{DS} = 800V, V_{GS} = -5/20V$		0.25		mJ
Turn-Off Switching Energy	$E_{OFF}$	$I_D = 40A, R_{G(ext)} = 2.5\Omega, L = 99\mu H$		0.05		
Turn-On Delay Time	$t_{d(on)}$	$V_{DS} = 800V, V_{GS} = -5/20V$		12		ns
Rise Time	$t_r$	$I_D = 40A, R_{G(ext)} = 2.5\Omega$		14		
Turn-Off Delay Time	$t_{d(off)}$	Inductive Load Timing relative to VDS Per IEC60747-8-4 pg 83		22		
Fall Time	$t_f$			4		
Internal Gate Resistance	$R_{G(int)}$	$f = 1MHz, V_{AC} = 25mV$		2.6		$\Omega$
Gate to Source Charge	$Q_{gs}$	$V_{DS} = 800V, V_{GS} = -5/20V, I_D = 40A$		34.3		nC
Gate to Drain Charge	$Q_{gd}$	Per IEC60747-8-4 pg 21		32.1		
Total Gate Charge	$Q_g$			92.1		

**Reverse Diode Characteristics:**

Characteristics	Symbol	Condition	Typ.	Max.	Units
Diode Forward Voltage	$V_{SD}$	$V_{GS} = -5V, I_{SD} = 20A$	3.6		V
		$V_{GS} = -5V, I_{SD} = 20A, T_J = 175^\circ C$	3.2		V
Continuous Diode Forward Current	$I_S$	$T_C = 25^\circ C$	44		A
Reverse Recovery Time	$t_{rr}$	$V_{GS} = -5V, I_{SD} = 50A, T_J = 25^\circ C$	43.4		ns
Reverse Recovery Charge	$Q_{rr}$	$V_R = 800V$	162		nC
Peak Reverse Recovery Current	$I_{mm}$	$di/dt = 1047A/\mu s$	8.1		A

**Thermal-Mechanical Specifications:**

Characteristics	Symbol	Condition	Specification	Units
Junction Temperature	$T_J$	-	-55 to +175	$^\circ C$
Storage Temperature	$T_{stg}$	-	-55 to +175	$^\circ C$
Typical Thermal Resistance Junction to Case	$R_{\theta JC}$	DC operation	1.24	$^\circ C/W$

**Ordering Information:**

Device	Package	Shipping
S2M0040120F	TO-247-4 full	30pcs/tube

**Ratings and Characteristics Curves**

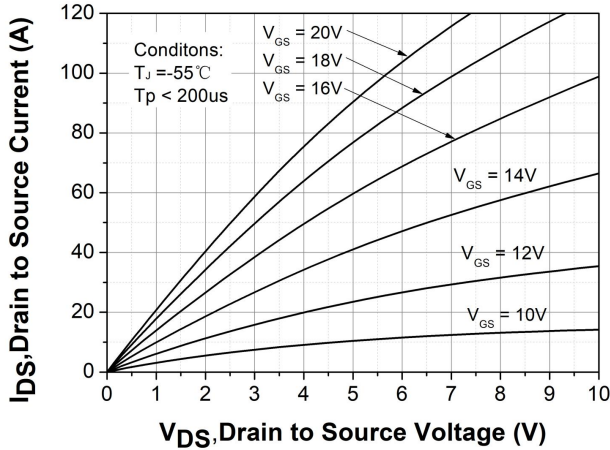


Figure 1. Output Characteristics  $T_J = -55^\circ\text{C}$

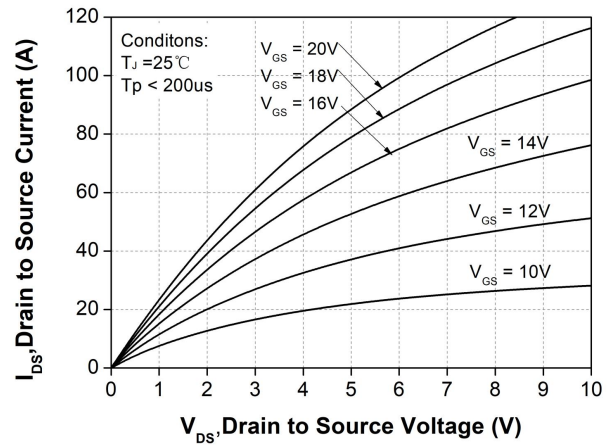


Figure 2. Output Characteristics  $T_J = 25^\circ\text{C}$

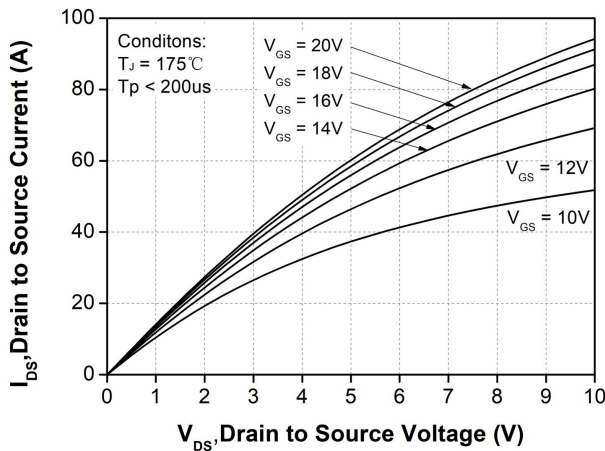


Figure 3. Output Characteristics  $T_J = 175^\circ\text{C}$

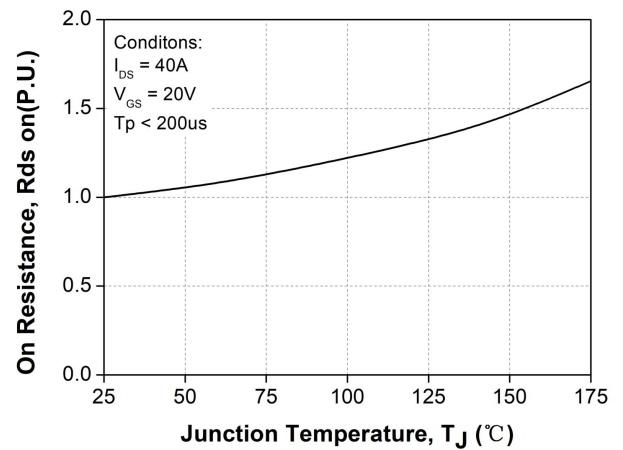


Figure 4. Normalized On-Resistance vs. Temperature

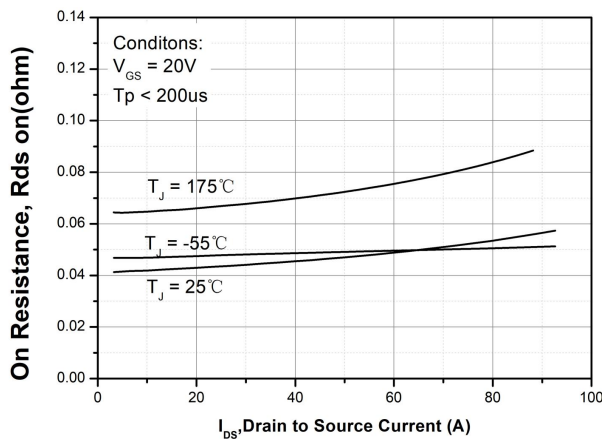


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

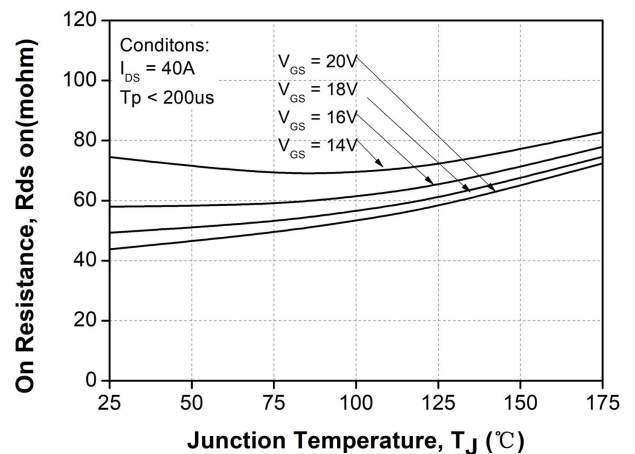
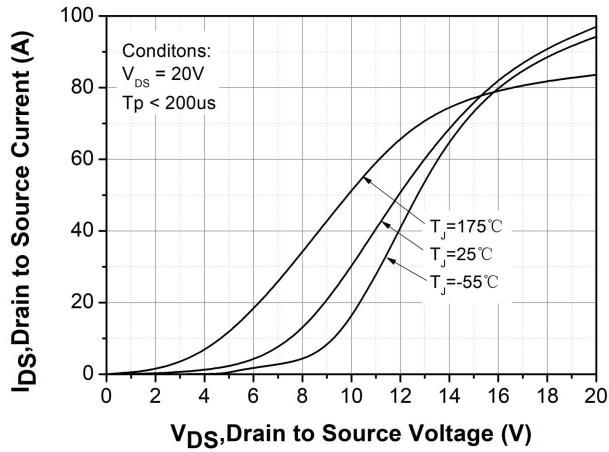
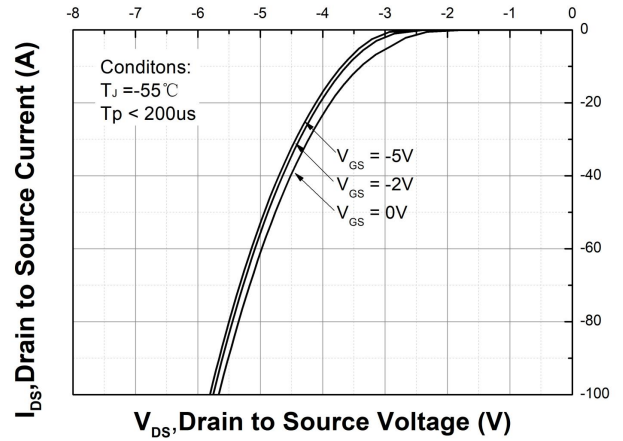


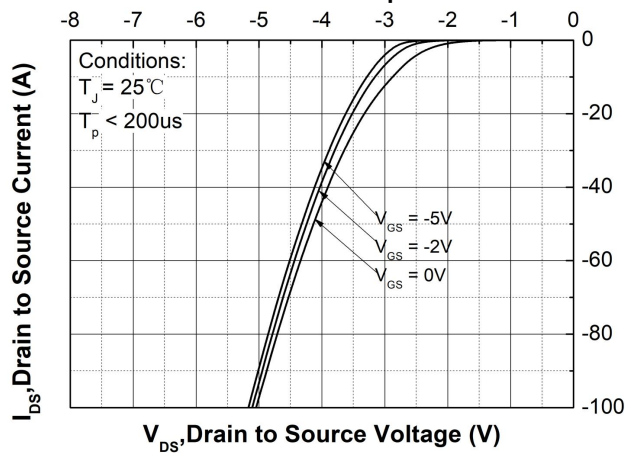
Figure 6. On-Resistance vs. Temperature For Various Gate Voltage



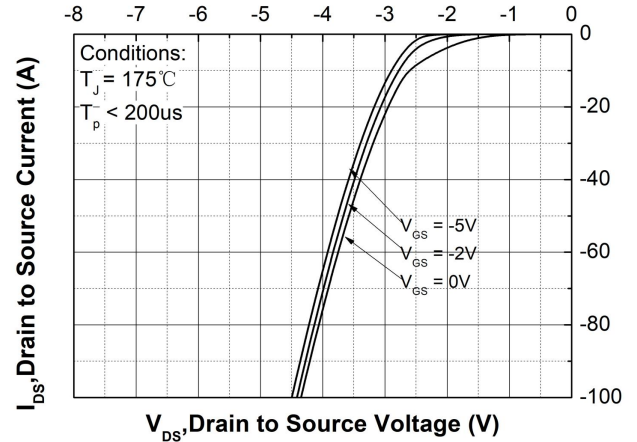
**Figure 7. Transfer Characteristic for Various Junction Temperatures**



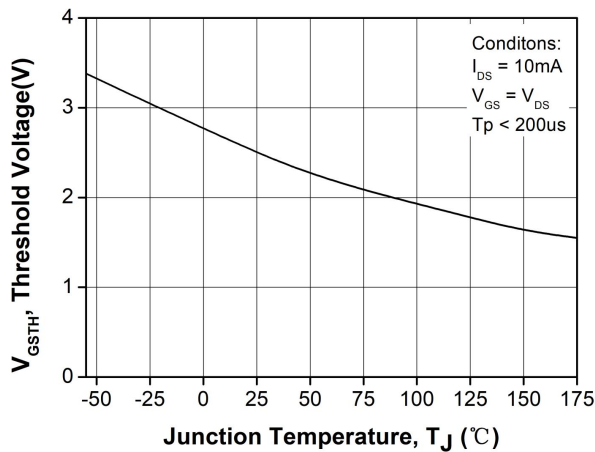
**Figure 8. Body Diode Characteristic at  $T_J = -55^\circ C$**



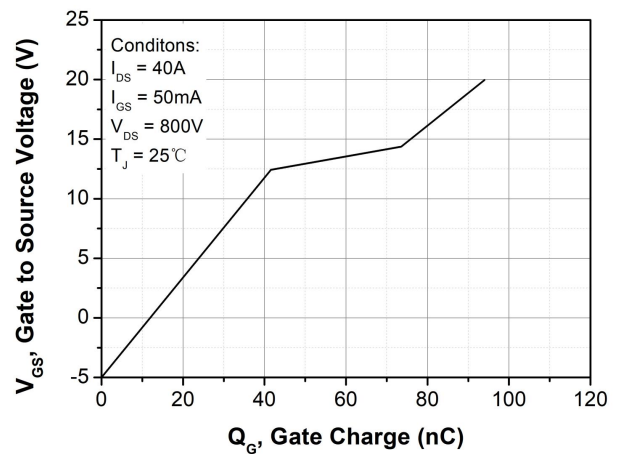
**Figure 9. Body Diode Characteristic at  $T_J = 25^\circ C$**



**Figure 10. Body Diode Characteristic at  $T_J = 175^\circ C$**



**Figure 11. Threshold Voltage vs. Temperature**



**Figure 12. Gate Charge Characteristic**

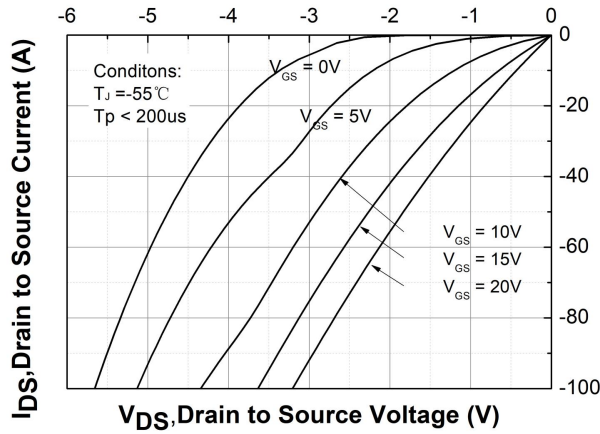


Figure 13. 3rd Quadrant Characteristic at  $T_J = -55^\circ\text{C}$

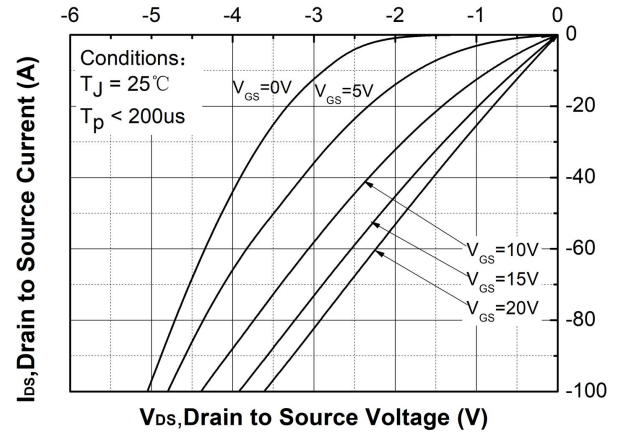


Figure 14. 3rd Quadrant Characteristic at  $T_J = 25^\circ\text{C}$

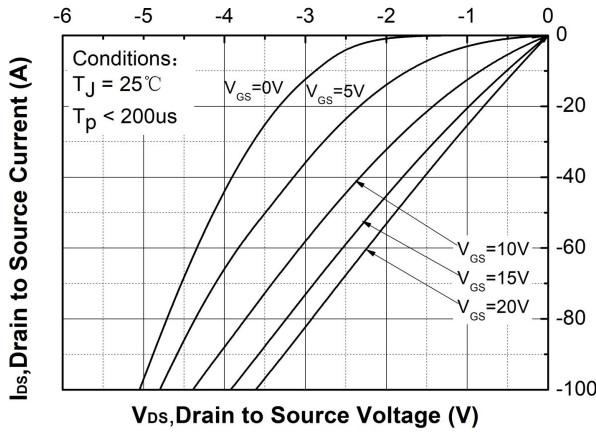


Figure 15. 3rd Quadrant Characteristic at  $T_J = 175^\circ\text{C}$

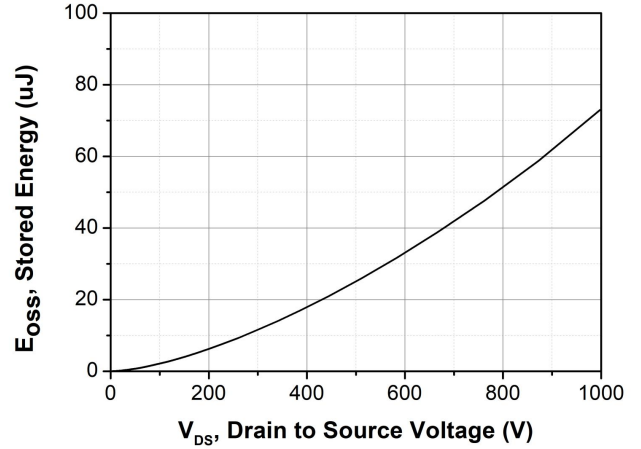


Figure 16. Output Capacitor Stored Energy

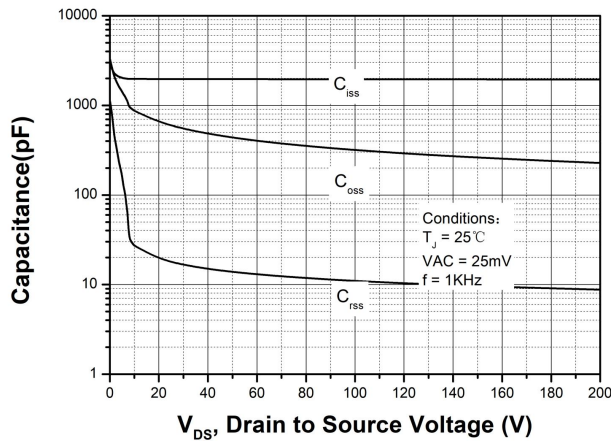


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

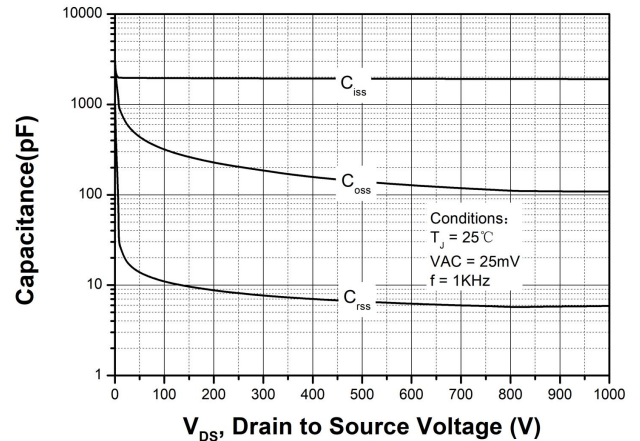
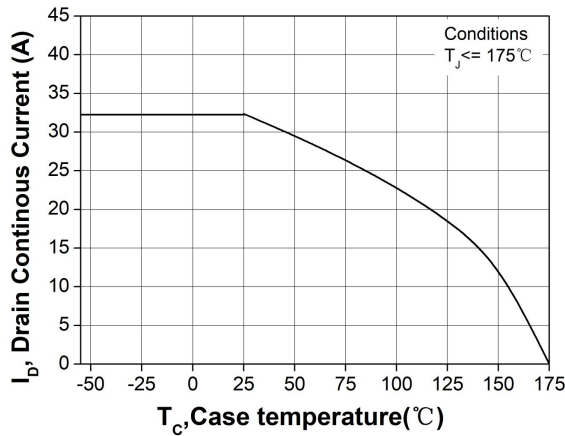
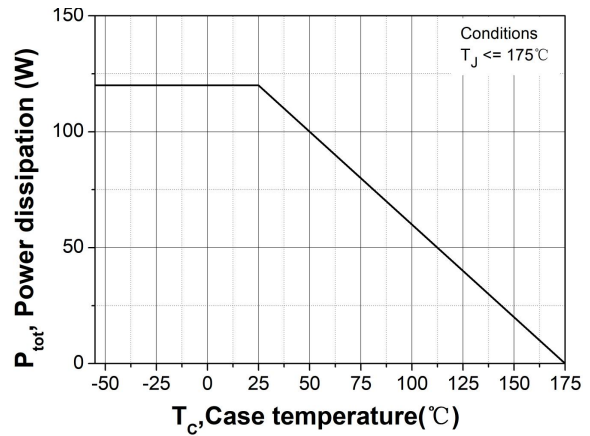


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

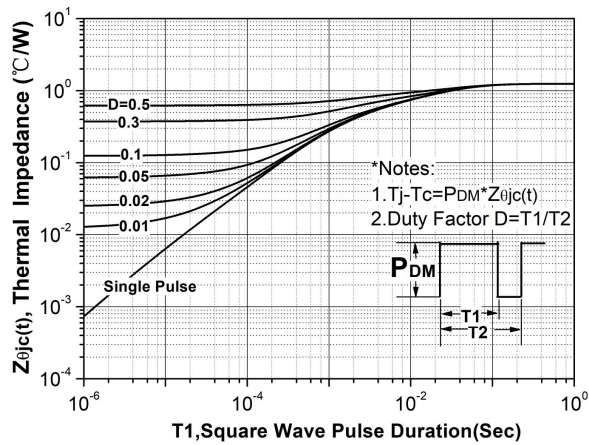




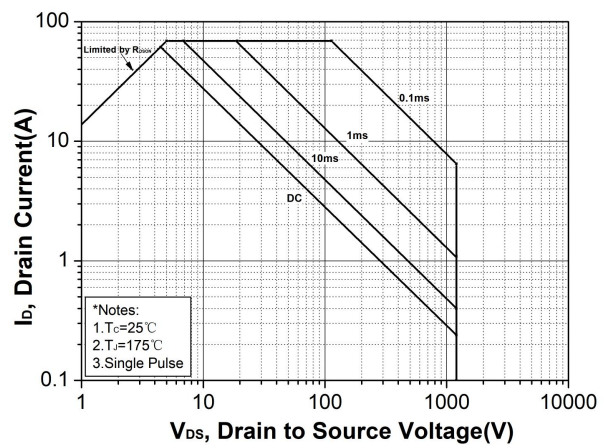
**Figure 19. Continuous Drain Current Derating vs. Case Temperature**



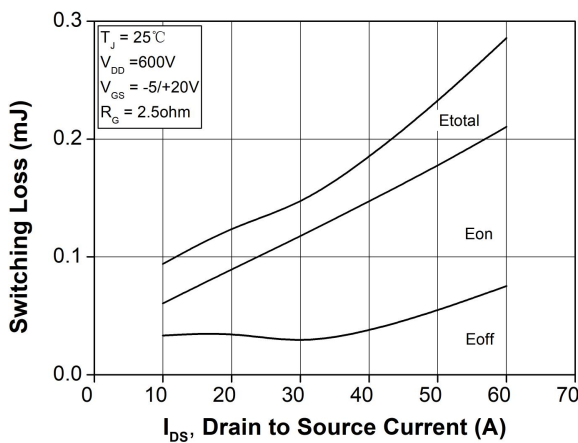
**Figure 20. Maximum Power Dissipation Derating vs. Case Temperature**



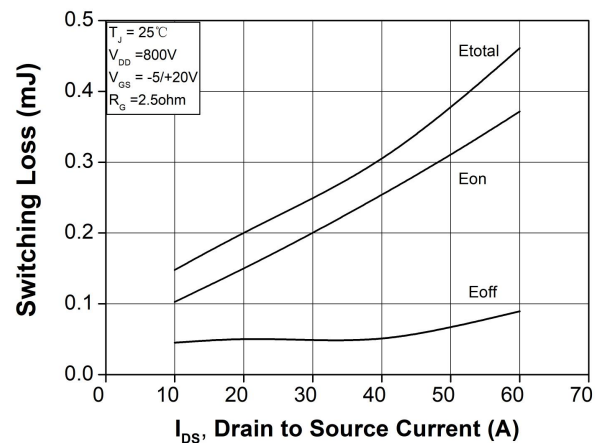
**Figure 21. Transient Thermal Impedance (Junction - Case)**



**Figure 22. Safe Operating Area**



**Figure 23. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 600V$ )**



**Figure 24. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 800V$ )**

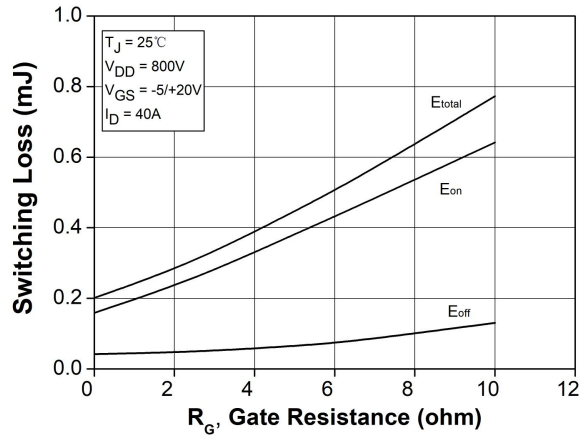


Figure 25. Clamped Inductive Switching Energy vs.  $R_{G(ext)}$

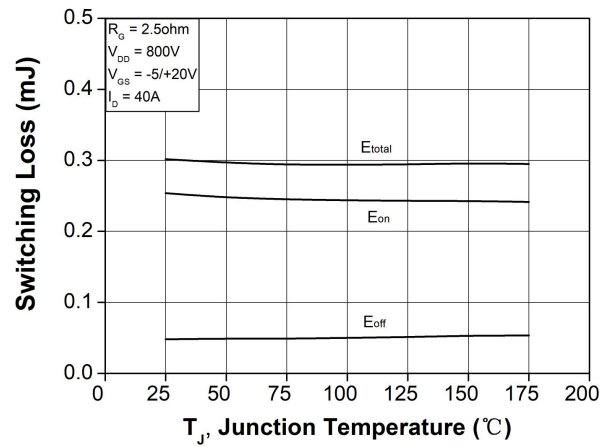


Figure 26. Clamped Inductive Switching Energy vs. Temperature

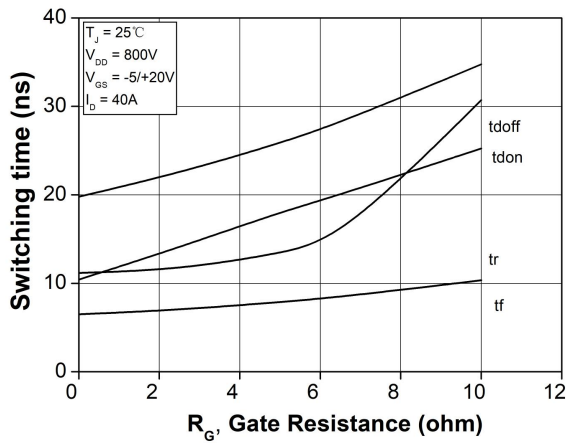


Figure 27. Switching Times vs.  $R_{G(ext)}$

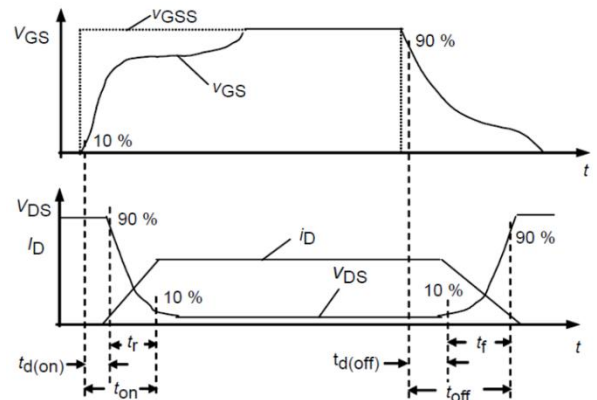
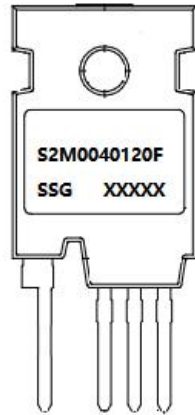


Figure 28. Switching Times Definition



**Marking Diagram**

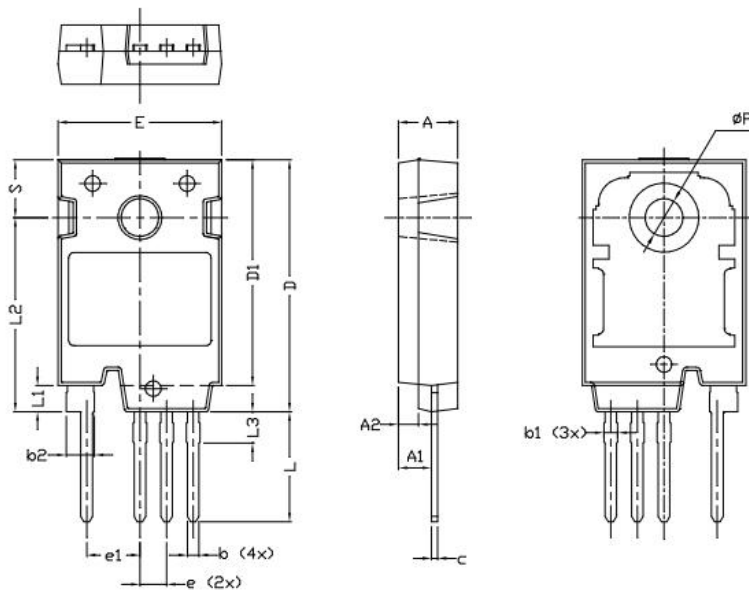


Where XXXXX is YYWWL

- S2M = Device Type
- 0040 = R<sub>DS(on)</sub>
- 120 = Reverse Voltage (1200V)
- F = Package
- SSG = SSG
- YY = Year
- WW = Week
- L = Lot Number

**Cautions:** Molding resin  
Epoxy resin UL:94V-0

**Mechanical Dimensions TO-247-4 full**



DIMENSIONS (mm)				
REF. DIM.	NOM	MIN	MAX	NOTES
A	--	4.85	5.15	
A1	2.50	2.20	2.60	
A2	1.27	--	--	
b	1.10	0.95	1.30	
b1	--	1.10	1.50	
b2	--	2.50	2.90	
c	--	0.40	0.80	
D	24	23.85	24.15	5
D1	21.50	--	--	
E	15.60	15.45	15.75	
e	2.54	--	--	
e1	5.08	--	--	
L	--	10.20	10.80	
L1	2.50	2.20	2.80	
L2	18.50	--	--	
L3	3.00	--	--	
øP	--	3.55	3.65	4
S	5.50	--	--	



S2M0040120F

Technical Data  
Data Sheet N2833, REV.-

RoHS

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